

End of the Year Seminars 2025

PhD student:

Andrea



Supervisors:

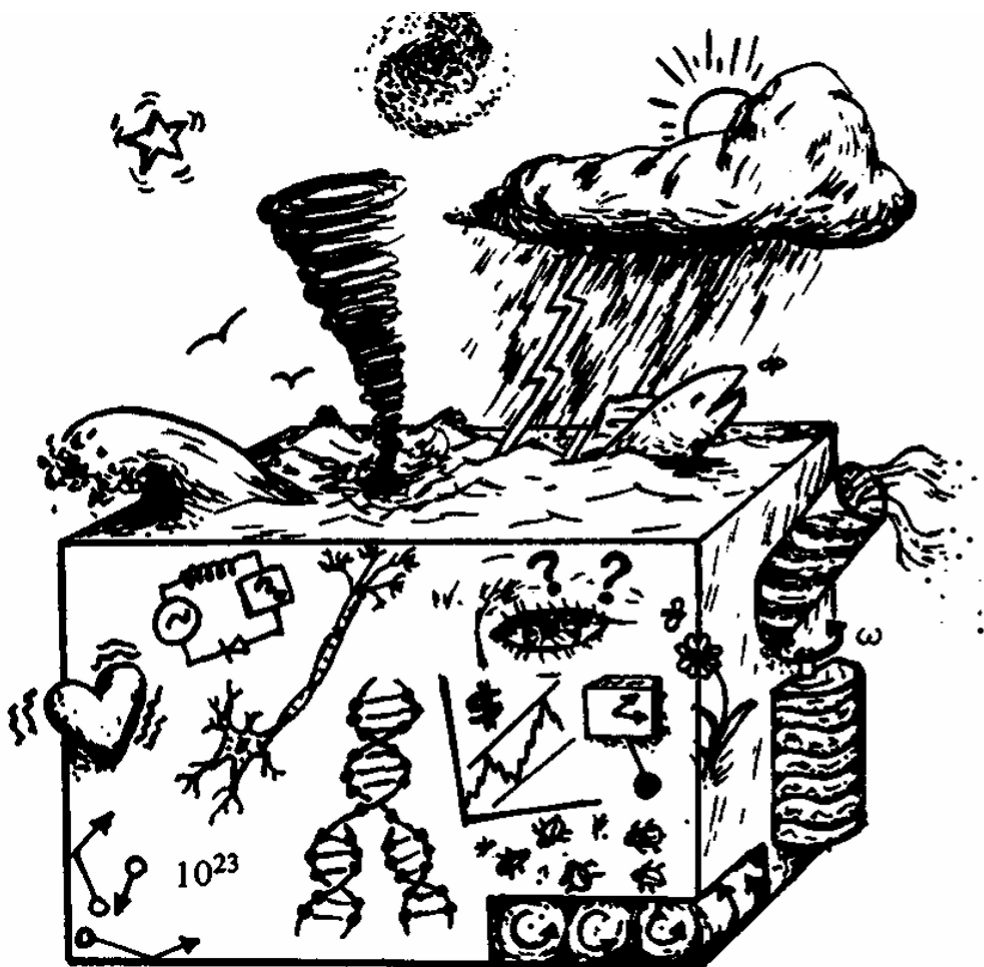
Paolo



Alessandro

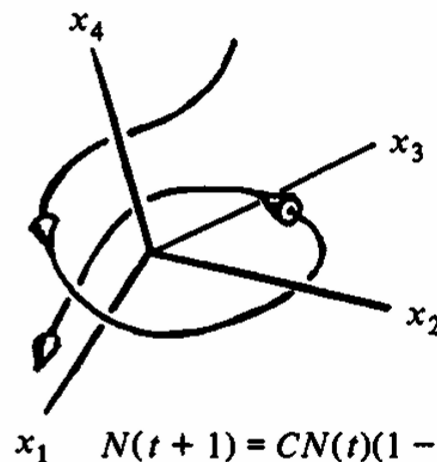


Quite **C**onfused **A**ctually



MODELS

$$\frac{dx_k}{dt} = F_K(x_1, \dots, x_4) \quad (K = 1, \dots, 4)$$

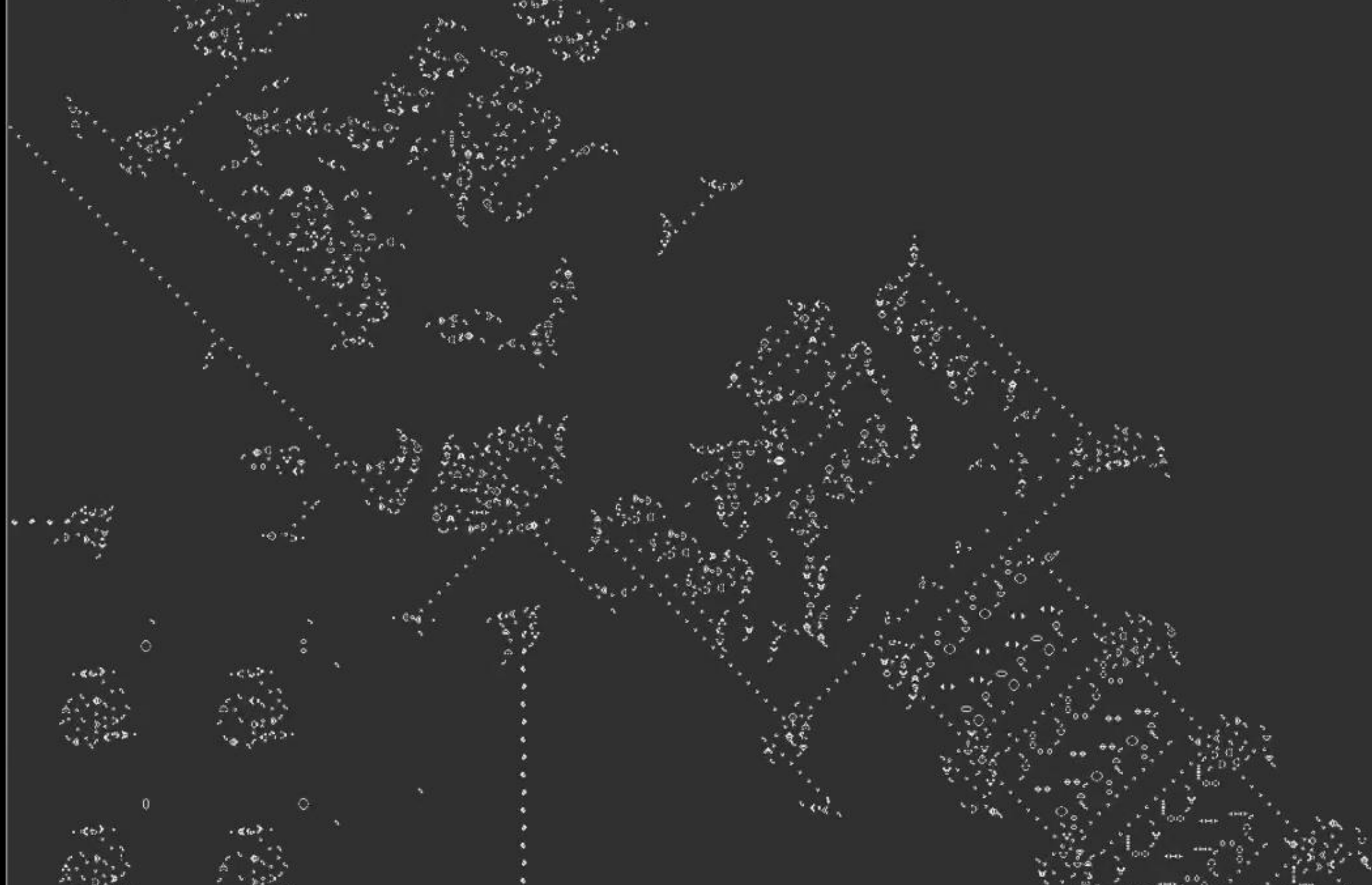


$$N(t+1) = CN(t)(1 - N(t))$$

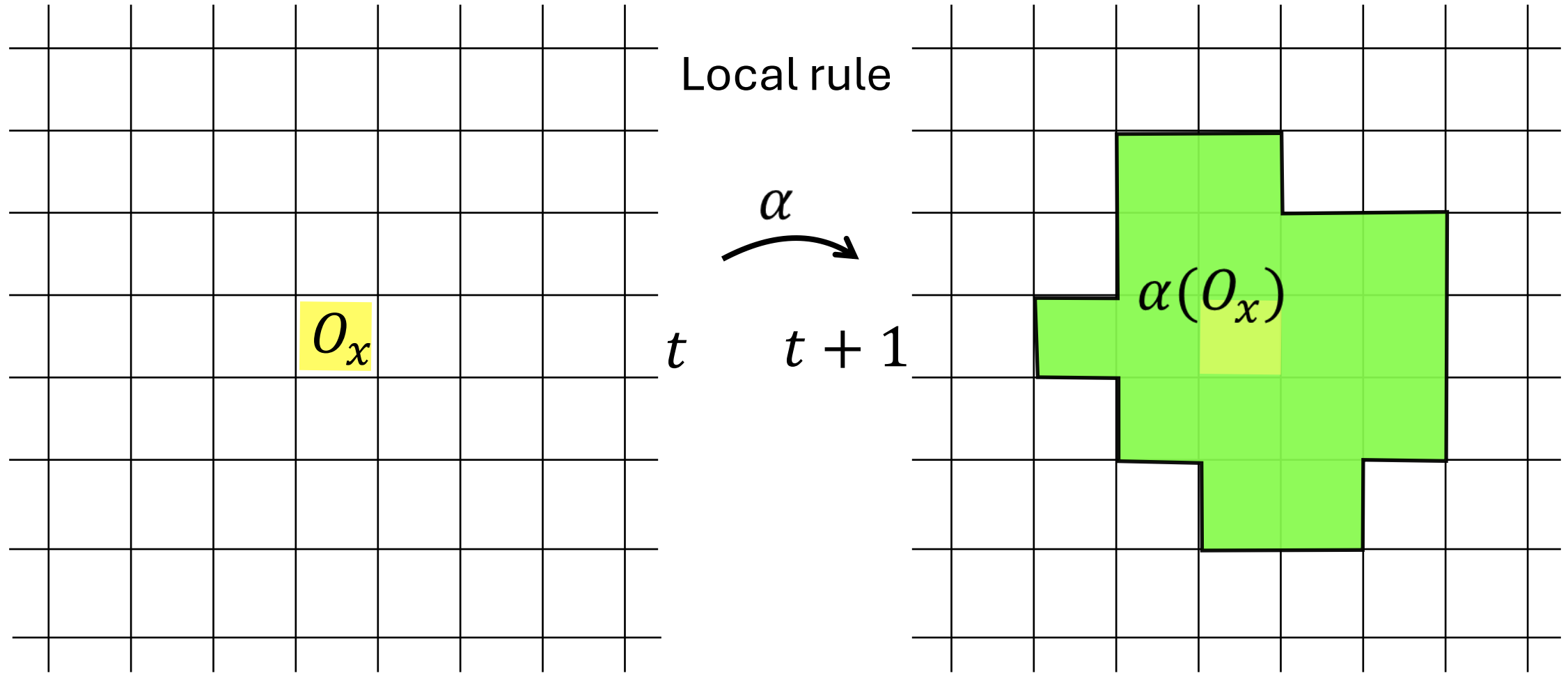
$$\frac{\partial U}{\partial t} + U \frac{\partial U}{\partial x} + \frac{\partial^3 U}{\partial x^3} = 0$$

$$S_i(t+1) = F(S_{i-1}(t), S_i(t), S_{i+1}(t))$$

ODE
PDE
SDE
FDE
MC
CA
PI
TN
VP
...



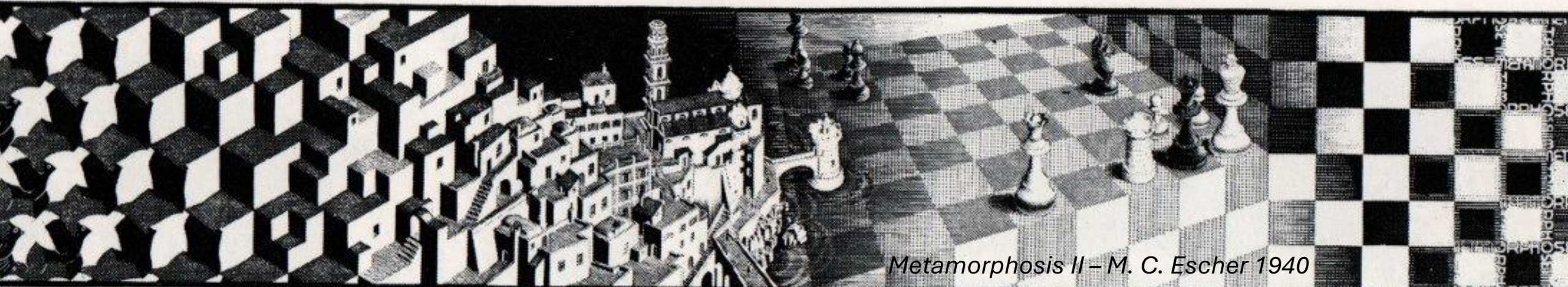
Quantum Cellular Automata



$$\alpha(O_x) = U^\dagger (O_x \otimes I) U$$

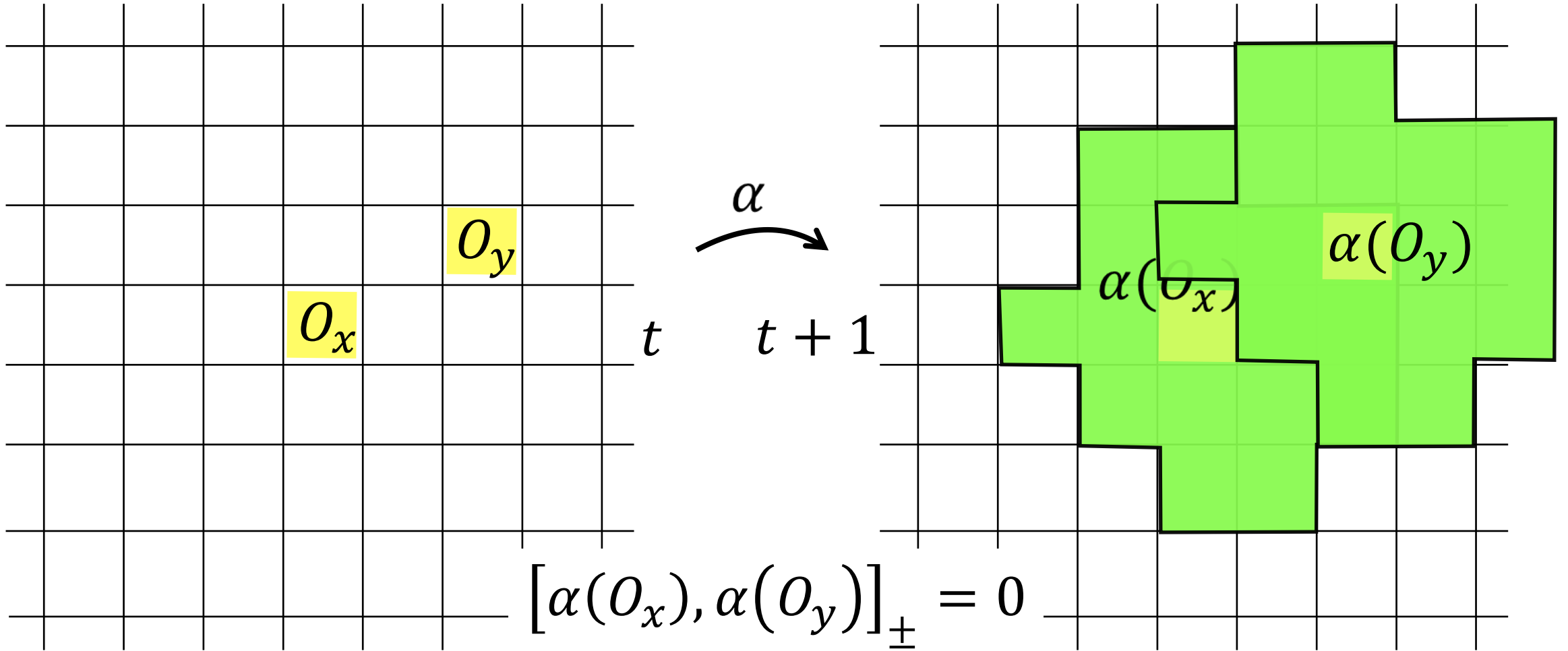
Goal of my PhD:

classify QCA beyond 1d



Metamorphosis II – M. C. Escher 1940

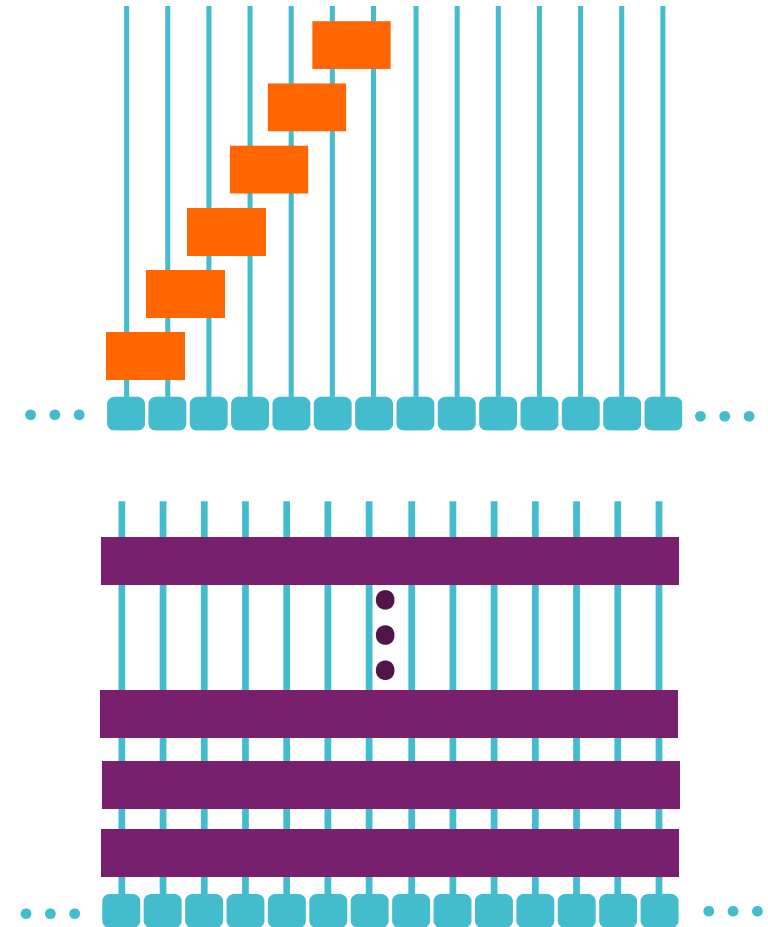
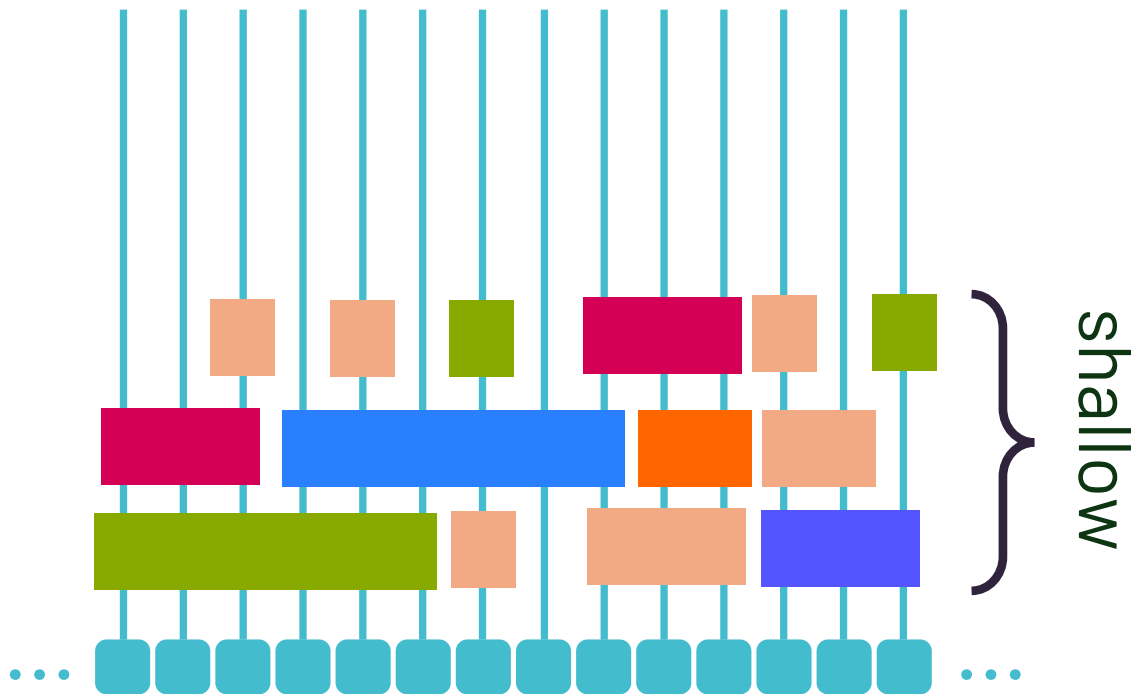
Classify: allowed Rules



Classify: QCA modulo circuits

Any circuit is a QCA, trivial.

but, not all QCA are circuits!



Quest for Complete Answers

- **1d** Done-ish. Safe to cite, unsafe to extend.
- **2d** “Define done”, there can be dragons.
- **3d+** Indistinguishable from folklore.

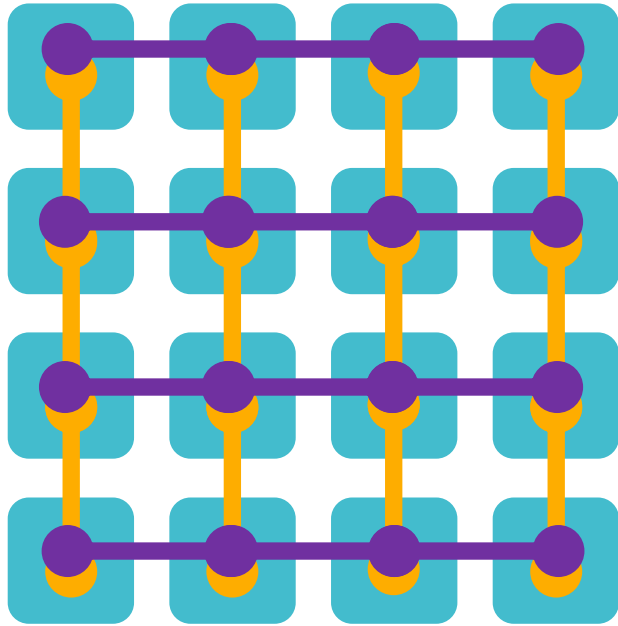
- **Topological order** Still arguing, politely.
- **Interacting QFT** Bless your heart.

Quick Checkpoint on Advances

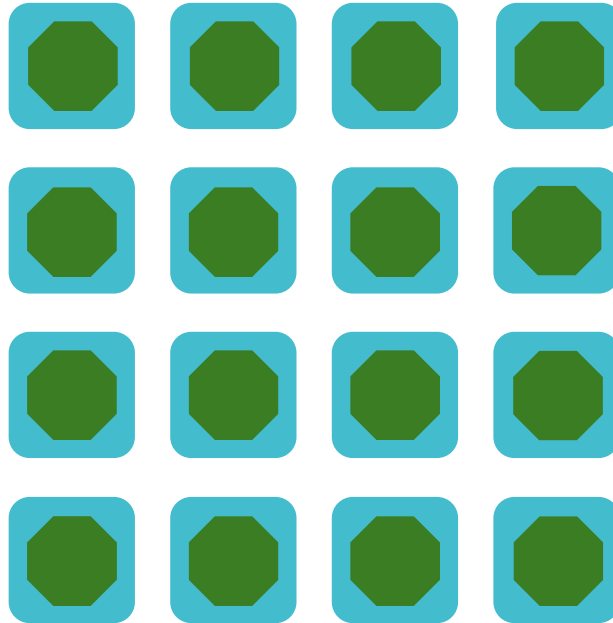
- ✓ Classified translation-invariant nearest-neighbor QCA on $\mathbb{Z}^d$
 - of qubits
 - of spinless (Majorana) fermions
- ✓ Classification modulo circuits in 2d (no fermions)

Update Rules for Qubits

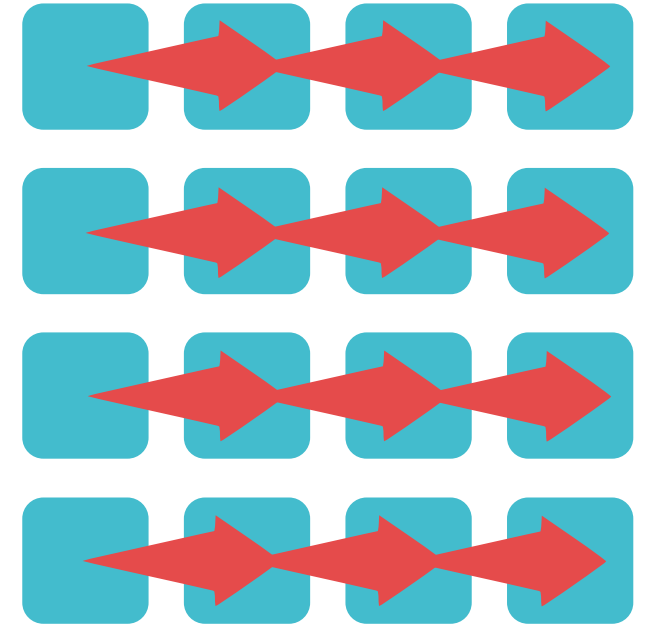
Multiply
Controlled-Phase



Site-wise
unitary



Shift



+ Update Rules for Majoranas

$$X = f^\dagger + f$$

$$Y = i(f^\dagger - f)$$



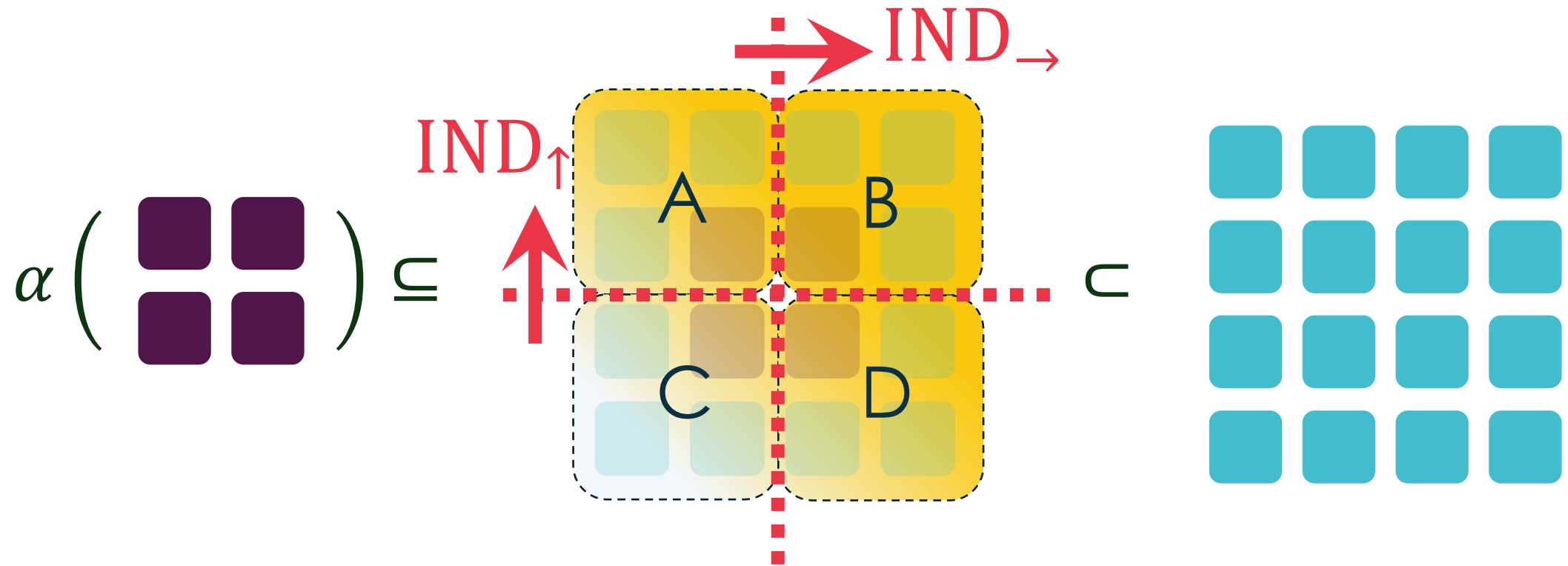
Majorana
Shift



Forking



Index: track information flow

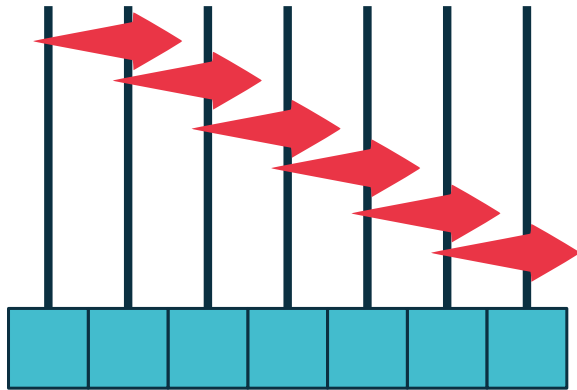


Index $\leftrightarrow$ classification mod circuits

Unique Equivalence Classes:

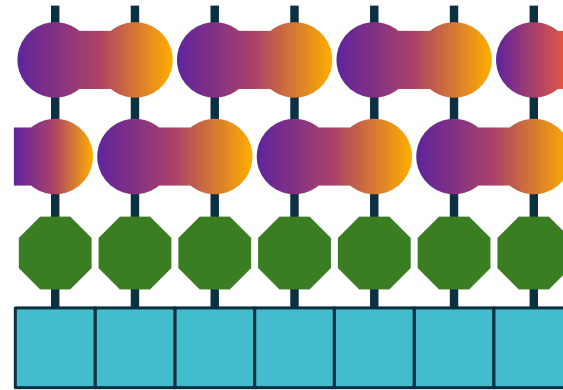
$$\overrightarrow{\text{IND}}(\alpha) \neq \vec{0}$$

Shifts



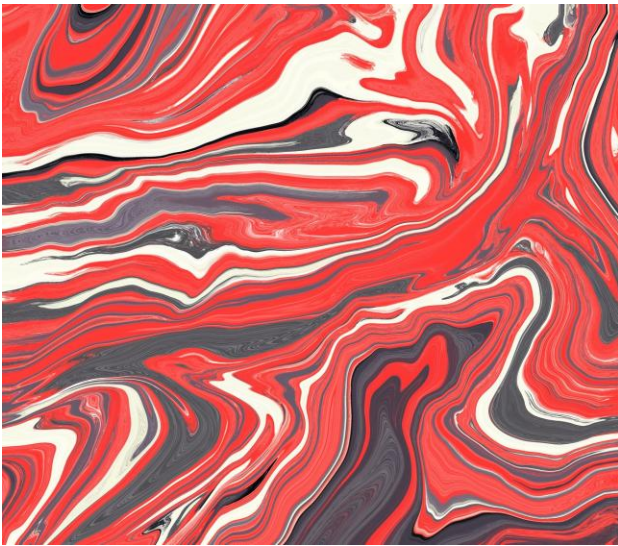
$$\overrightarrow{\text{IND}}(\alpha) = \vec{0}$$

Shallow Circuits

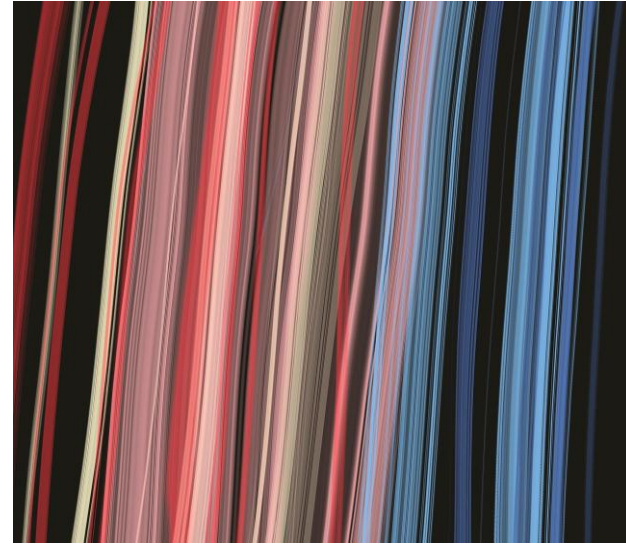


Non-compact 2d (no fermions)

*Any 2d QCA is a circuit followed by
an ∞ bunch of shifts*



$\sim$



No 1d invariants (unless for transl-inv case)

Non-compact 2d

- *Any d circuit is equivalent to a regular circuit*

